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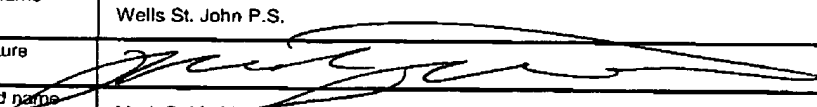
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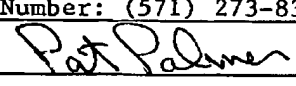
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/672,816
	Filing Date	September 26, 2003
	First Named Inventor	Terry L. Gilton
	Art Unit	1765
	Examiner Name	Binh X. Tran
Total Number of Pages in This Submission	Attorney Docket Number	MI22-2398

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Signature			
Printed name	Mark S. Matkin		
Date	12-9-05	Reg. No.	32,268

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DEC 9 2005**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. 10/652,816
Filing Date September 26, 2003
Confirmation No. 8918
Inventor Terry L. Gilton
Assignee Micron Technology, Inc.
Group Art Unit 1765
Examiner Binh X. Tran
Attorney's Docket No. MI22-2398
Customer No. 021567
Title Semiconductor Processing Methods

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VIA FACSIMILE

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
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Spokane, WA 99201-3828

Responsive to the Notice of Allowance dated November 21, 2005,
Applicant amends and remarks as follows:

AMENDMENTS